## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No	10/323,525
Priority Filing Date	December 18, 2002
Inventor	Alan R. Reinberg
Assignee	Micron Technology, Inc.
Priority Group Art Unit	2824
Priority Examiner	Christian D. Wilson
Attorney Docket No	
Title: Microelectronic Device Fabricating Methods,	and Methods of Forming
a Pair of Field Effect Transistor Gate Lines of Diffe	rent Base Widths From a
Common Deposited Conductive Layer	

## INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 10/323,525, filed December 18, 2002. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Mark S. Matkin Reg. No. 32,268 Form PTO-1449

## U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. MI22-2491

SERIAL NO. PRIORITY 10/323,525

LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)

APPLICANT: Micron Technology, Inc.

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U.S. PATEN	NT DOC	UMENTS								
*Examiner's Initials		Document Number	Date	Name		Class	Subclass	Filir II Apj	g Date propriate	
	AA	4,704,218	11/1987	Giammarco et al.		438	421			
	AB	4,597,826	07/1986	Majima et al.		216	46			
	AC	4,648,937	03/1987	Ogura et al.		438	702			
	AD	4,857,477	08/1989	Kanamori		438	386			
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	АН	5,895,740	04/1999	Chien et al.		430	313			
	AJ	6,103,596	08/2000	Peng		438	439			
FOREIGN F	PATENT	DOCUMENTS	<b>-</b>							
		Document Number	Date	Country	Class	SubclasS	Translation			
	AJ							Yes `	No	
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OTHER RE	FEREN	CES (including Autho	r, Title, Date, Pe	rtinent Pages, Etc.)				<u></u>	<u> </u>	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449

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APPLICANT: Micron Technology, Inc.

FILING DATE **GROUP** PRIORITY December 18, PRIORITY 2824 2002 U.S. PATENT DOCUMENTS \*Examiner's Initials Document Date Name Class Subclass Filing Date Number If Appropriate AA 6,103,613 08/2000 Akram 438 597 6,140,227 10/2000 Chen et al. 438 644 AC 6,211,051 B1 04/2001 Jurgensen et al. 438 597 AD 05/2001 6,232,229 B1 Reinberg 438 669 6,242,321 B1 06/2001 Acosta et al. 438 424 6,509,626 01/2003 Reinberg 257 623 AG ΑН FOREIGN PATENT DOCUMENTS Document Date Country Class SubclasS Translation Number Yes No A.J AL. OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) AM AN AO **EXAMINER** DATE CONSIDERED

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